TEK-004

Ŧ	10B Code Selection Logic	D1.0/ 011101 0100 100010 1011 balance the running disparity of the encoded bit stream	D5.0/ 101001 1011 101001 0100 balance the running disparity of the encoded bit stream	/D26.0/ 010110 1011 010110 0100 balance the running disparity of the encoded bit stream	/D15.0/ 010111 0100 10100 1011 balance the running disparity of the encoded bit stream
-	B for t RD+	1011	0100	0100	0 101.1
in Time	10B Code for Current RD	100010	101001	010110	101000
	for RD-	0100	1011	1011	0100
D	10B 10B Code for Code for Current RD+	011101	101001	010110	010111
C	10B Code Name	/D1.0/	/D2.0/	/D26.0/	/D15.0/
<b>e</b>	Primary Bit Stream (8B words)	000 00001	000 00101	000 11010	000 01111
	Prin S (8B)	000	000	000	000
Y A	Sequence Number		2	3	4

FIG. 1 (PRIOR ART)

C D	Q		田	Ŧ	G
(   	10]	8	10B	Additional	
Code   Code for	Code	for	Code for	Bit	10B Code Selection Logic
Name   Curren	Currer	It RD-	Current RD-Current RD+	Stream	
					10B Code selected to
/D1.0/  011101 0100  100010 1011	01110	0100	100010 1011	0	represent the bit of the
				-	additional bit stream
					10B Code selected to
/D5.0/  101001 1011  101001 0100	10100	1011	101001 0100	1-1	balance the running disparity
		·			of the encoded bit stream
	-				10B Code selected to
000 11010   /D26.0/  01011	01011	0 1011	0110 1011 010110 0100	-	represent the bit of the
	•		,		additional bit stream
					10B Code selected to
/D15.0/  01011]	01011]	0100	0111 0100 101000 1011		balance the running disparity
		,			of the encoded bit stream

FIG. 2

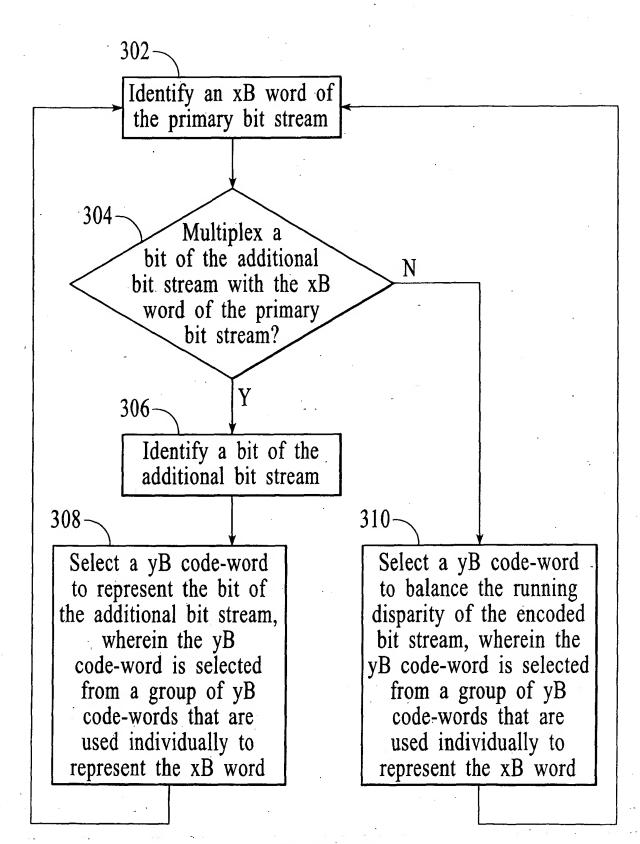
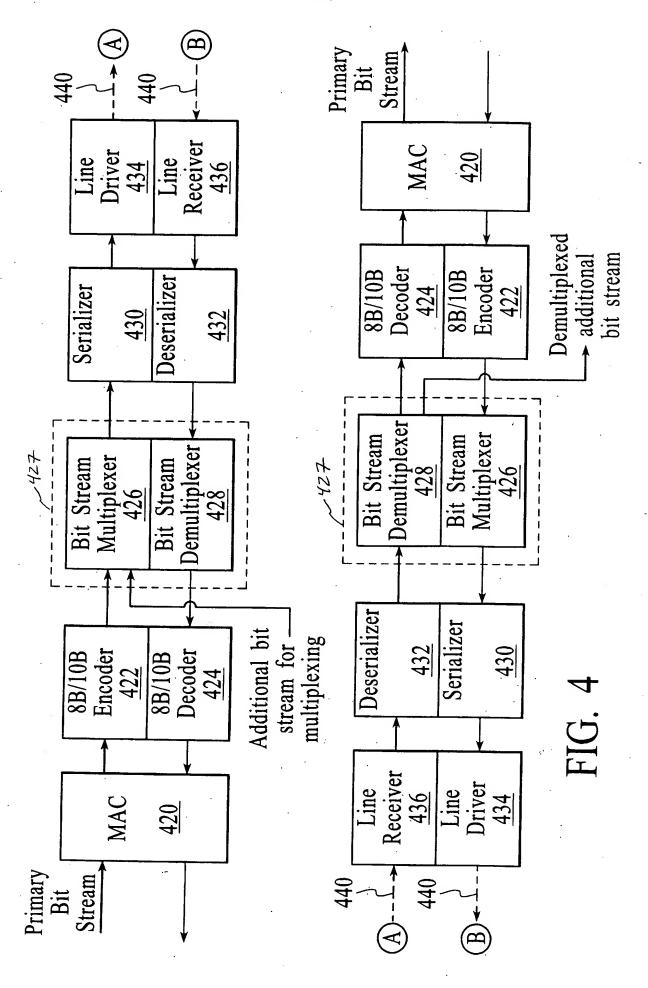
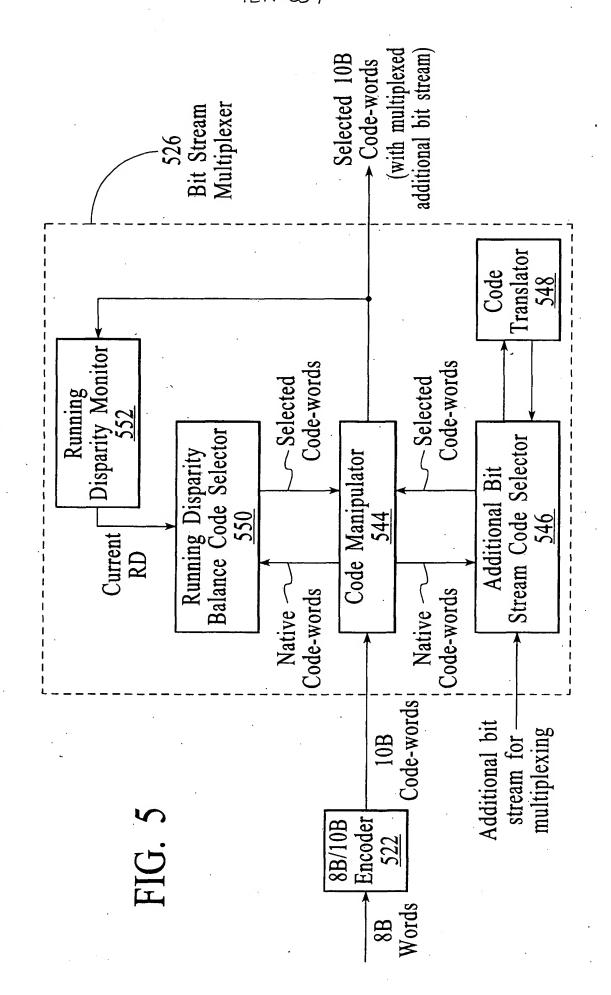


FIG. 3

TEK-004





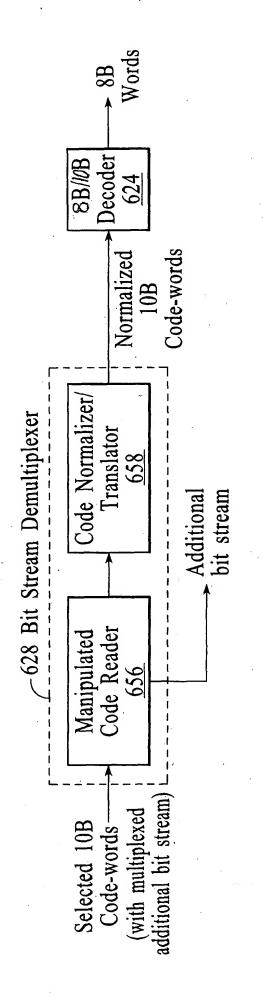


FIG. 6

1 EA= VVI -- + + 3-

TEK-004

Z	RD After Code Selection	I	+	+	+	+	I	l	+	+	+	+	1	+
M	Selected 10B Code	١	+	+	ł	١	1	1	+	+	1	, 1	١	+
L	RD Before Selected Code 10B Selection Code	1	١	+	<b>+</b>	+	· +	١	1	<b>-</b>	+	+	+	1
K	Multiplexed Additional Bit Stream	0				0		0		<del></del> .		0		
F	RD After Code Selection	ı	+.	. 1	ı	ŧ	+	1	+	ı	1	ı	+	ı
H	Selected 10B Code	+	+	1	-+-	+	+	i	+	1	+	<del>,+</del>	+	
Н	RD Before Selected Code 10B Selection Code	ı	١.	+	ı	ı	1	+	1	+	. 1	1	1	+
Ŋ	New RD	S	<u> </u>	ഥ	S	S	[ <u>_</u>	ĽZ.	لتم	[ <u>*</u>	S	S	ш	CT.
$\mathbf{F}_{(-)}$	10B Code for New Current RD+ RD (DC Balance) (-or Neutral)	100010 1011 S	101001 0100 F	010110 0100 F	101000 1011 S	010001 1101 S	010100 1100 F	101000 1010 F	001100 0110 F	010100 1001 F	100001 1110 S	000111 1001 S	101000 1001 F	011010 0100 F
$\mathbf{E}_{(+)}$ $\mathbf{F}_{(-)}$ $\mathbf{G}$	to +	0100010	-			101110 0010 010001	101011 0011 010100 1	010111 1010 101000	110011 0110   001100	101011 1001 010100	<del></del>	_	_	-
$D = E_{(t)} \qquad F_{(\cdot)} \qquad G$	for 10B Code for D- Current RD+ (DC Balance) (-or Neutral)	00 100010	10101 11	1011 010110	0100 101000	101110 0010 010001	101011 0011 010100 1	010111 1010 101000	001100	101011 1001 010100	100001	000111 1	010111 1001   101000	010110
$C$ $D$ $E_{(+)}$ $F_{(-)}$ $G$	10B Code for 10B Code for Current RD+ (DC Balance) (DC Balance) + or Neutral)	/D1.0/ 011101 0100 100010	/D5.0/ 101001 1011 101001	010110 1011 010110	/D15.0/ 010111 0100 101000	/D29.4/  101110 0010   010001 1	/D31.3/  101011 0011   010100 1	/D15.5/  010111 1010   101000	/D24.6/ 110011 0110 001100	/D31.1/ 101011 1001 010100	/D30.7/ 011110 0001 100001 1	/D7.1/   111000 1001   000111	/D15.1/  010111 1001   101000 1	/D22.0/ 011010 1011 011010
$\mathbf{B}$ $\mathbf{C}$ $\mathbf{D}$ $\mathbf{E}_{(+)}$ $\mathbf{F}_{(-)}$ $\mathbf{G}$	Code Current RD- Current RD+ (DC Balance) Name (+ or Neutral) (- or Neutral)	/D1.0/ 011101 0100 100010	00101   05   /D5.0/   101001 1011   101001	/D26.0/ 010110 1011 010110	11111 OF /D15.0/ 010111 0100 101000	1101   9D  /D29.4/ 101110 0010  010001 1	.1111   7F  /D31.3/ 101011 0011  010100 1	11111   AF  /D15.5/ 010111 1010   101000	/D24.6/ 110011 0110 001100	11111   3F  /D31.1/  101011 1001   010100	11110   FE  /D30.7/  011110 0001   100001 1	)0111   27   /D7.1/   111000 1001   000111 1	)   2F  /D15.1/  010111 1001   101000 1	10110   16   /D22.0/ 011010 1011   011010

FIG. 7

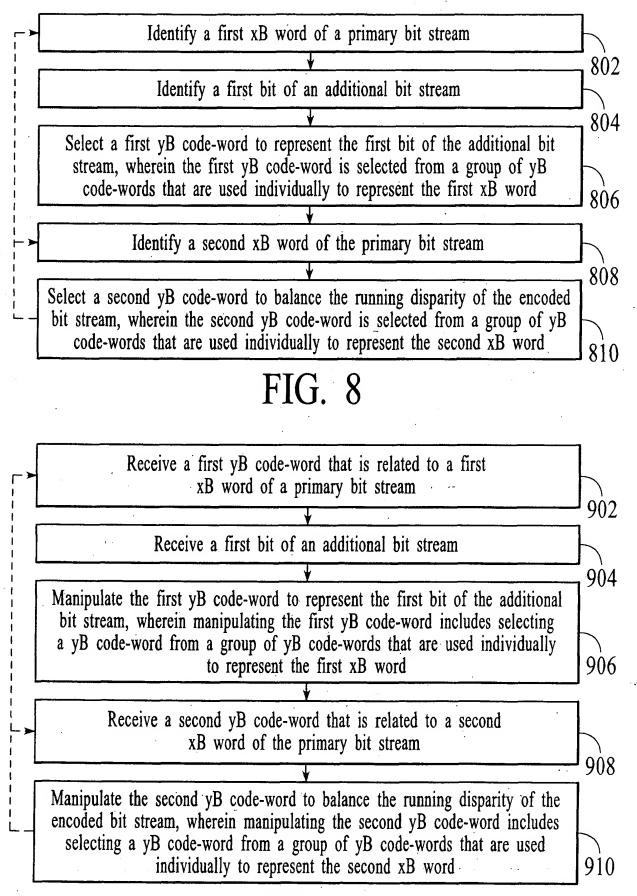
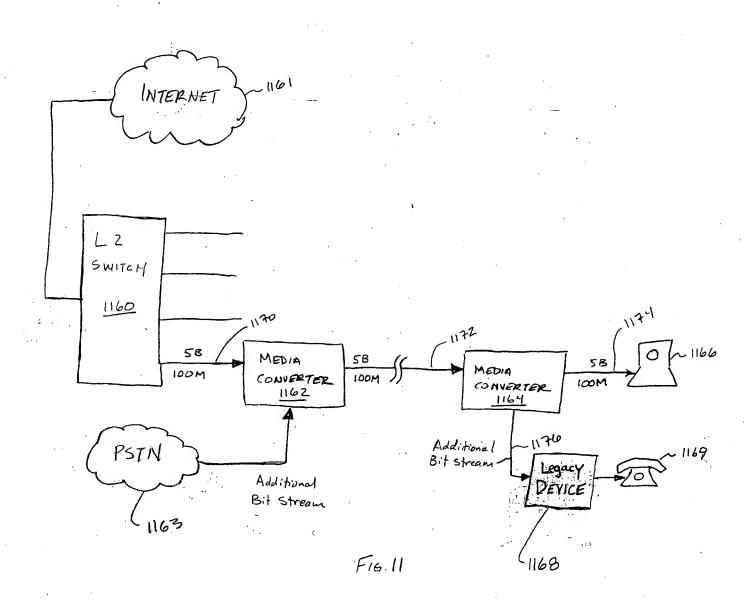
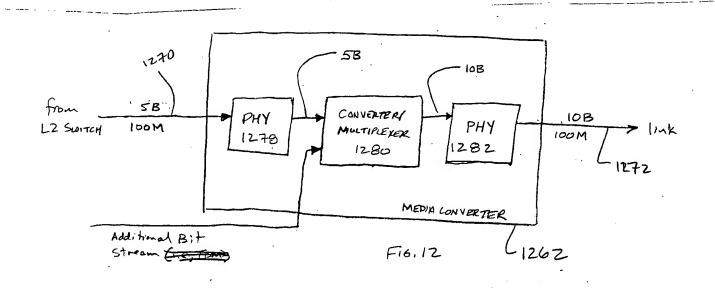


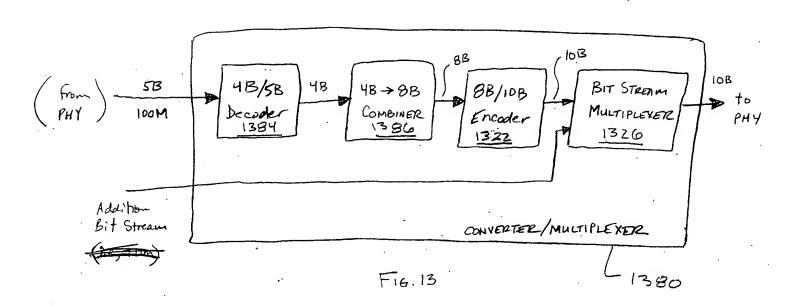
FIG. 9

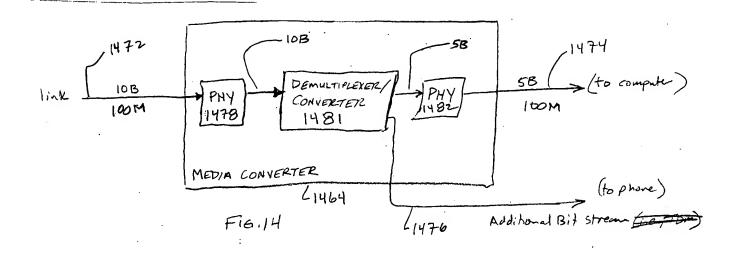
Receive a first yB code-word that represents	
an xB word of a primary bit stream	
¥	•
Receive a first bit of an additional bit stream	
<b>.</b>	
Select a first yB code-word to represent the first bit from the a bit stream, wherein the yB code-word is selected from a group code-words that are used individually to represent the same xl as the first yB code-word represents	of yB
Output the selected yB code-word	
Receive a second yB code-word that represents an xB word of the primary bit stream	
Select a yB code-word to balance the running disparity of the bit stream, wherein the yB code-word is selected from a group code-words that are used individually to represent the same xF as the second yB code-word represents	of yB
• •	
Output the selected yB code-word	•
Output the selected yB code-word	

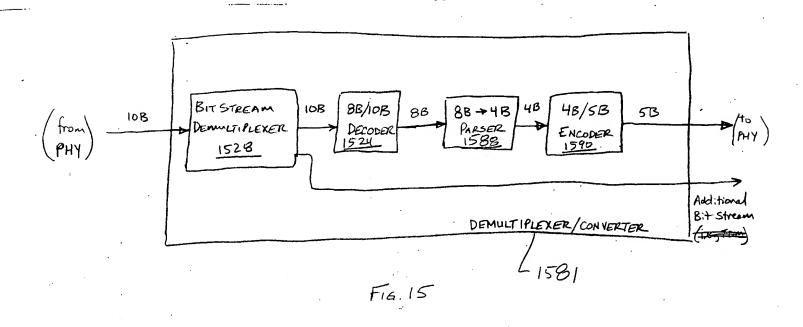
FIG. 10

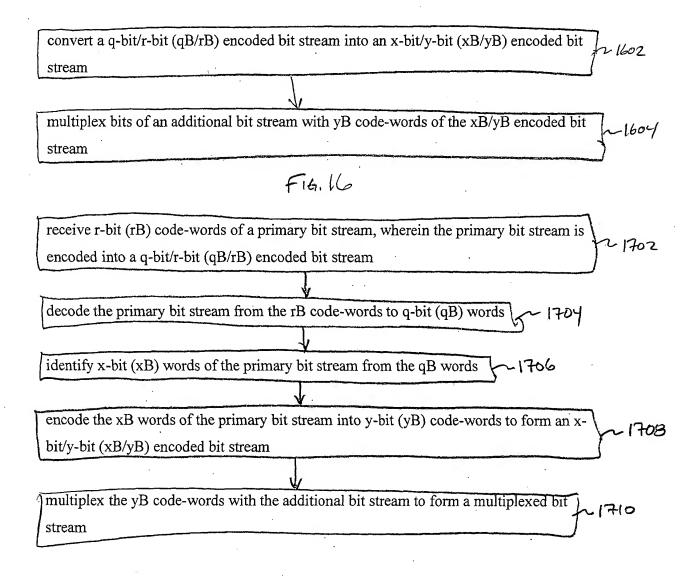












F14.17

demultiplex bits of an additional bit stream from y-bit (yB) code-words of an x-bit/y-bit 2 1802 (xB/yB) encoded bit stream convert the xB/yB encoded bit stream into a q-bit/r-bit (qB/rB) encoded bit stream F16.18 receive a multiplexed bit stream that includes y-bit (yB) code-words of a primary bit stream and bits of an additional bit stream, wherein the primary bit stream is encoded into 1902 an x-bit/y-bit (xB/yB) encoded bit stream demultiplex the multiplexed bit stream into separate streams of yB code-words and bits of the additional bit stream decode the yB code-words into x-bit (xB) words 1906 identify q-bit (qB) words from the xB words encode the qB words into r-bit (rB) code-words to form a q-bit/r-bit (qB/rB) encoded bit stream F16.19